

# P.S. KARTHIKA, B.A, DEE, B. Tech, M.E

+ 973 38396192

 [karthikavignesh1021@gmail.com](mailto:karthikavignesh1021@gmail.com)

## Educational Qualifications

- M.E – Electronics and Communication Engineering (VLSI DESIGN) (2015-2017) Coimbatore Institute of Technology (Government Aided Autonomous Institution, Coimbatore, India. First class with 8.10 out of 10 (CGPA).
- B. Tech – Electronics and Communication Engineering (ECE) (2011-2014) Dr.B.R.Ambedkar Institute of Technology Pondicherry University, Port Blair, India. First class with 8.1 out of 10 (CGPA)
- Diploma in Electrical Engineering (EE) (2007 - 2010) Dr.B.R.Ambedkar Institute of Technology, Maharashtra State Board of Technical Education First class with 69.33%.
- B.A - Bachelor of Arts (ENGLISH) (2014) Alagappa University -Distance Education.
- Central Board of Secondary Education (AISSE)(2007) – Andaman Nicobar Islands.

## Work Experiences

- PROJECT OFFICER Energy Conservation Building Code in All India Institute of Local Self-Government (AII LSG) from November 2018 to December 2019.
- GUEST LECTURER in Dr.B.R.Ambedkar Institute of Technology in Port Blair, Andaman from Jan 2018 to May 2018 and September 2018 to October 2018.
- LECTURER in Bhuvan Polytechnic College, Bangalore in Electrical Department from July 2017 to January 2018

## Area of Interest

- Electrical machine. Power System, Digital electronics, Mixed circuits CMOS Design

## Work Related Certificates/Training/ Workshops

- Certificate of Excellence in AutoBotz AVR workshop conducted by Technophilia Systems with Robotics & Computer Applications Institute of the USA held at Dr.B.R. Ambedkar Institute of Technology, Port Blair on 20th&21st Sept 2013.
- Internship Certificate for one-month training Embedded System Design at TECH INNOVATES EMBEDDED SOLUTIONS at Chennai from 20th May to 20th June 2013.
  - Programming based on 8051 and AVR microcontrollers.
  - Introduction to Arduino software and ARM processors.
- Certificate of Proficiency in Interaction course on “Control Automation Instrumentation and Measurement” by CARIMO cell, IIT Bombay from Jan 28th to 30th 2013.
  - Basics of the Control system, PID controllers, introduction to the embedded system.
  - Automation using SCADA- boiler automation, distributed control system.

- One week in-plant training and Television Broadcast in Doordarshan Kendra, Port Blair from 26th Dec 2011 - 30th Dec 2011.
- Two weeks of in-plant training in BSNL, Port Blair from 28th June 2010 - 3rd July 2011.
- One-month Industrial training in Surya Chakra Power Plant, Port Blair from 1st June 2009 - 30th June 2009.
- Certificate on VB.NET in Continuing Education Centre, Dr.B.R. Ambedkar Institute of Technology from 11.09.2013 to 28.09.2013.
- One-week industrial training in Electrical Section at Naval Ship Repair Yard, Port Blair from 8th Dec 2009 - 14th Dec 2009.
- Certificate of Merit three-month course in AutoCAD from SIS Computer Education with "A" Grade on 01.08.2009 to 31.10.2009.
- Certificate on Paper Presentation in "ANALYSIS OF HYBRID FULL ADDER using 65 nm TECHNOLOGY" at Hindustan College on 19th Feb 2016.
- Certificate on Technical Quality Enhancement program in "BASIC GRAPH THEORY ALGORITHMS II" at Coimbatore Institute of Technology held on even semester 2016.
- Certificate on Attended online one day workshop in "RTL DESIGN AND FUNCTIONAL VERIFICATION" at Maven Silicon on 17th July 2016.

## Technical Skills

- Software tools: Auto CAD, PLC, Mat lab, Multisim, Keil  $\mu$  vision Ride, Proteus ISIS professional, AVR studio
- Programming language: Assembly language Embedded.

## Project Details

- ME VLSI DESIGN Project in Design of low power high-speed level shifter using the multi-supply-voltage
  - The project aim is to Design of low power high-speed level shifter using multi-supply voltage
- ME VLSI DESIGN Mini -project - The project aim is to Design a low power Carry Select Adder using BEC-1 converter.
- B. Tech Project in Voice controlled wheelchair - The project aim is to control the wheelchair by means of voice command for a paralyzed person.
- Diploma Project in Design, Installation & Wiring of a residential building - The project aim is to design, installation & wiring of a residential building.
- Diploma Mini - Project in study of 1028 seated auditorium design, load calculation and safety system.
- Project Certificate in Embedded System Design at TECH INNOVATES EMBEDDED SOLUTIONS at Chennai.
  - Programming based on 8051 and AVR microcontrollers.
  - Introduction to Arduino software and ARM processors.

## Research Article Publications

- Sowmiya J, Karthika P.S, Dr.S.UmaMaheswari, Puvaneswari G, " Design Of Level Shifter By Using Multi-Supply Voltage" International Research Journal of Engineering and Technology(IRJET),e-ISSN: 2395-0056p-ISSN: 2395-0072, Volume: 03 Issue:11 | Nov-2016(Annexure II).
- Karthika.P.S, SowmiyaJ, Brunda KaleeswariK, MurlidharanK "Area Efficient Full Adder Design for Low Power Application" International Journal of Scientific Engineering and Research (IJSER), ISSN: 2347-3878, Volume: 04 Issue: 12 | Dec -2016 (Annexure II).
- Muralidharan .K, Karthika P.S, Sowmiya J, Sohail Akbar, "Gradient Histogram Estimation and Preservation for Image Denoising Using Dwt" International Research Journal of Engineering and Technology (IRJET), e-ISSN: 2395-0056p-ISSN: 2395- 0072 , Volume: 03 Issue: 11 | Nov- 2016(Annexure II).

## Personal Details

- Name - P.S.Karthika
- D.O. B - 22 – 11 – 1991
- Gender - Female (Married)
- Passport - U0675472
- Present Address - Flat No – 23A, 2nd Floor, Mansoori Plaza, Building No.3304, Block - 333, UmAlhassam, Kingdom of Bahrain

## Declaration

I, hereby declare that the information contained herein is true and correct to the best of my knowledge and belief.



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P.S.KARTHIKA, B.Tech, M.E



Reg.No. 15 71 006/RG

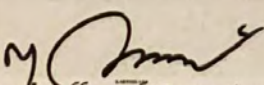


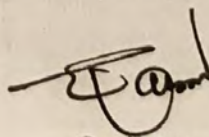
*The Syndicate of the Anna University hereby makes known that*  
**KARTHIKA P S** *has been admitted to the* **DEGREE OF MASTER OF ENGINEERING in VLSI DESIGN** *under the Faculty of Information and Communication Engineering, having completed the prescribed programme of study through* **COIMBATORE INSTITUTE OF TECHNOLOGY, COIMBATORE,** *an autonomous college affiliated to this University and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in* **FIRST CLASS** *at the Examination held in*  
**APRIL 2017.**

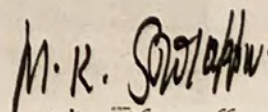
*Given under the Seal of the University*



Chennai 600 025  
India  
June 2018  
GPF 041700079

  
Controller of Examinations

  
Registrar

  
Vice-Chancellor  
BAUWAOBCHCECHCECHCECHCECH





# COIMBATORE INSTITUTE OF TECHNOLOGY

(Autonomous Institution Affiliated to Anna University, Chennai)

COIMBATORE - 641 014

SERIAL No. : B 000208

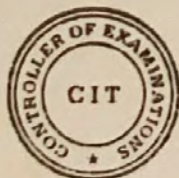
INDIA

## CONSOLIDATED GRADE SHEET



NAME KARTHIKA.P.S.		REGISTER No. 15 71 006	PERIOD OF STUDY 2015 - 2017
DATE OF BIRTH 22-11-1991	PROGRAMME AND BRANCH M.E. VLSI DESIGN		

SEMESTER	COURSE CODE	COURSE NAME	CREDIT EARNED	GRADE	No. OF ATTEMPTS	MONTH AND YEAR OF PASSING
I	15MVD11	GRAPH THEORY AND OPTIMIZATION TECHNIQUES	4	B+	1	DEC ' 2015
	15MVD12	ADVANCED DIGITAL SYSTEM DESIGN	3	A	1	DEC ' 2015
	15MVD13	CMOS VLSI DESIGN	3	B+	1	DEC ' 2015
	15MVD14	SYSTEM DESIGN USING FPGA	3	B+	1	DEC ' 2015
	15MVD15	ANALOG VLSI CIRCUITS	3	B	1	DEC ' 2015
	15MVDE03	NANO ELECTRONICS	3	B+	1	DEC ' 2015
	15MVD16	VLSI DESIGN LABORATORY - I	2	A	1	DEC ' 2015
	15MVD17	SEMINAR AND TECHNICAL WRITING	1	A+	1	DEC ' 2015
II	15MVD21	LOW POWER CMOS CIRCUITS AND MEMORIES	3	A	1	MAY ' 2016
	15MVD22	MIXED SIGNAL CIRCUIT DESIGN	3	A	1	MAY ' 2016
	15MVD23	TESTING OF VLSI CIRCUITS	3	A+	1	MAY ' 2016
	15MVD24	CAD FOR VLSI CIRCUITS	3	A	1	MAY ' 2016
	15MVDE14	GENETIC ALGORITHMS FOR VLSI	3	B+	1	MAY ' 2016
	15MVDE09	ADVANCED EMBEDDED SYSTEM DESIGN	3	A	1	MAY ' 2016
	15MVD25	VLSI DESIGN LABORATORY - II	2	A+	1	MAY ' 2016
	15MVD26	MINI PROJECT	2	A+	1	MAY ' 2016
III	15MVDE18	VLSI SIGNAL PROCESSING	3	A+	1	AUG ' 2016
	15MVDE23	ARTIFICIAL INTELLIGENCE AND OPTIMIZATION TECHNIQUES	3	O	1	AUG ' 2016
	15MVDE06	DSP INTEGRATED CIRCUITS	3	A	1	AUG ' 2016



*R/S*

(R.SRIRAM)

CONTROLLER OF EXAMINATIONS

P.T.O.

DATE : 31-08-2017

THIS DOCUMENT IS ISSUED WITHOUT ANY CORRECTION



# Pondicherry University



*The Vice-Chancellor and the Executive Council of the Pondicherry University, on the recommendations of the duly appointed Board of-Examiners, hereby declare that*

**KARTHIKA, P.S.**

*has been admitted to the Degree of*

## BACHELOR OF TECHNOLOGY

*in Branch :* ELECTRONICS AND COMMUNICATION ENGINEERING

*and placed in* FIRST CLASS

*at the Examination*

*held in* MAY 2014.

*He / She was a bonafide student of*

DR. B.R. AMBEDKAR INSTITUTE OF TECHNOLOGY.

*Given under the Seal of the University*



*M. Ramachandran*

Registrar

Puducherry

Date

21 NOV 2016

*Anisabasheer*

Vice-Chancellor



NAME : P.S. KARTHIKA Course : B.Tech. ( Electronics and Communication Engg. )

COLLEGE : Dr.B.R.Anbedkar Institute of Technology, Port Blair (10413)

REGISTER No. 10T18337

YEAR OF PASSING : MAY '14

SEMESTER	SUBJECT	CREDIT GRADE	YEAR OF PASSING	SEMIESTER	SUBJECT	CREDIT GRADE	YEAR OF PASSING
3	MATHEMATICS - III	4	MAY '12	7	MICROWAVE AND OPTICAL ENGINEERING	4	NOV '13
3	ELECTRICAL ENGINEERING	3	NOV '11	7	EMBEDDED SYSTEMS	4	NOV '13
3	DATA STRUCTURES AND ALGORITHMS	3	NOV '11	7	ELECTIVE: SPECIAL TOPICS IN COMMUNICATION ENGINEERING	3	NOV '13
3	ELECTRON DEVICES	4	NOV '11	7	COMMUNICATION LABORATORY - III	2	NOV '13
3	CIRCUIT THEORY	4	NOV '11	7	EMBEDDED SYSTEMS LABORATORY	2	NOV '13
3	ENGINEERING ELECTROMAGNETICS AND WAVES	4	NOV '11	7	SEMINAR	1	NOV '13
3	ELECTRICAL ENGINEERING LAB	2	NOV '11	7	INDUSTRIAL VISIT/ TRAINING	2	NOV '13
3	DATA STRUCTURES AND ALGORITHMS LAB	2	NOV '11	7	PROJECT WORK	3	MAY '14
3	ELECTRON DEVICES LAB	2	NOV '11	7	INDUSTRIAL MANAGEMENT	4	MAY '14
4	NUMERICAL METHODS AND TECHNIQUES	4	MAY '12	8	TELECOMMUNICATION SWITCHING NETWORKS	3	MAY '14
4	ELECTRONIC CIRCUITS - I	4	MAY '12	8	ELECTIVE: CELLULAR MOBILE COMMUNICATION	3	MAY '14
4	SIGNALS & SYSTEMS	4	MAY '12	8	ELECTIVE: SATELLITE COMMUNICATION SYSTEMS	2	MAY '14
4	NETWORKS & TRANSMISSION LINES	4	MAY '12	8	ADVANCED COMMUNICATION LABORATORY	2	MAY '14
4	DIGITAL CIRCUITS	4	MAY '12	8	COMPREHENSIVE VIVA	2	MAY '14
4	ANALOG COMMUNICATION SYSTEMS	2	MAY '12	8	PROFESSIONAL ETHICS	1	MAY '14
4	ELECTRONIC CIRCUITS - I LAB	2	MAY '12	8	PROJECT WORK	6	MAY '14
4	DIGITAL CIRCUITS LABORATORY	2	MAY '12	8	*****End of Semester*****		
4	COMMUNICATION LABORATORY - I	3	MAY '12				
4	PHYSICAL EDUCATION	1	MAY '12				
5	PROBABILITY AND RANDOM PROCESSES	4	NOV '12				
5	ELECTRONIC CIRCUITS - II	4	NOV '12				
5	SYSTEM DESIGN USING ICS	4	NOV '12				
5	DIGITAL SIGNAL PROCESSING	4	NOV '12				
5	LINEAR AND DIGITAL CONTROL SYSTEMS	4	NOV '12				
5	WAVEGUIDES ANTENNAS & WAVE PROPAGATION	4	NOV '12				
5	ELECTRONIC CIRCUITS - II LABORATORY	2	NOV '12				
5	SYSTEM DESIGN USING ICS LABORATORY	2	NOV '12				
5	NETWORK & TRANSMISSION LINES LABORATORY	2	NOV '12				
5	GENERAL PROFICIENCY - I	2	NOV '12				
6	INFORMATION THEORY & CODING	4	MAY '13				
6	DIGITAL COMMUNICATION	4	MAY '13				
6	COMPUTER & COMMUNICATION NETWORKS	3	MAY '13				
6	MICROPROCESSOR & MICROCONTROLLER	3	MAY '13				
6	ELECTIVE: VLSI DESIGN	3	MAY '13				
6	ELECTIVE: SOFT COMPUTING	3	MAY '13				
6	COMMUNICATION LAB - II	2	MAY '13				
6	COMPUTER NETWORKS LAB	2	MAY '13				
6	MICROPROCESSOR AND MICROCONTROLLER LAB	2	MAY '13				
6	GENERAL PROFICIENCY - II	2	MAY '13				
7	ENGINEERING ECONOMICS	3	NOV '13				

DATE: 14/07/2014



CGPA 8.10 CLASS First Class

SEAL:





# Maharashtra State Board of Technical Education

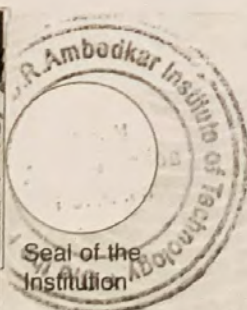
**P. S. KARTHIKA**

*Karthika.*

the withinsigned  
having successfully completed the  
prescribed course of studies and having  
passed the final examination with  
First class, has been awarded the

## DIPLOMA IN ELECTRICAL ENGINEERING

on behalf of the Government of  
Maharashtra on the 09 day  
of the month of December in the  
year 2010. In testimony whereof  
is set the seal and signature of the  
Director, Maharashtra State Board  
of Technical Education.



*Shankar*  
DIRECTOR





# Maharashtra State Board of Technical Education

## STATEMENT OF MARKS OBTAINED BY AWARD OF DIPLOMA CERTIFICATE

AN0012140

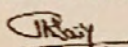
MR. / MS. P.S. KARTHIKA

ENROLLMENT NO. : 7020050

EXAMINATION : SUMMER 2010

TERM : EVEN

PROGRAMME : EE DIPLOMA IN ELECTRICAL ENGINEERING

CODE & TITLE OF COURSE	COURSE HEAD	MAX. MARKS	MIN. MARKS	MARKS OBTAINED	CREDITS		
EE203 Electrical Science	TH	100	40	56	6		
	PT	25	0	16			
	AG	125	50	88			
	PR	50	20	34			
	TW	25	10	16			
EE205 Electrical Measurements	TH	100	40	75	6		
	PT	25	0	17			
	AG	125	50	92			
	PR	25	10	19			
	TW	25	10	18			
EE401 Power System - I	TH	100	40	55	4		
	PT	25	0	19			
	AG	125	50	74			
EE402 Power System - II	TH	100	40	80	4		
	PT	25	0	12			
	AG	125	50	92			
EE403 Electrical Machines - I	TH	100	40	41	8		
	PT	25	0	13			
	AG	125	50	54			
	PR	50	20	41			
	TW	25	10	20			
EE404 Electrical Machines - II	TH	100	40	70	8		
	PT	25	0	16			
	AG	125	50	86			
	PR	50	20	47			
	TW	25	10	20			
EE405 Project - I	OR	25	10	20	4		
	TW	75	30	65			
EE406 Project - II	OR	50	20	42	7		
	TW	100	40	76			
EE408 Power System - III	TH	100	40	68	8		
	PT	25	0	15			
	AG	125	50	83			
	PR	50	20	41			
	TW	25	10	20			
EE505 Control Of Electrical Machine	TH	100	40	70	6		
	PT	25	0	15			
	AG	125	50	85			
	PR	25	10	20			
EE506 Manufacturing System For Electrical Machines	TW	25	10	15	6		
	TH	100	40	64			
	PT	25	0	15			
	AG	125	50	79			
	TW	25	10	17			
DATE : 07-10-2010		PROGRAMME MINIMUM CREDIT	200	TOTAL MAX. MARKS	RESULT WITH %	TOTAL MARKS OBTAINED	Credit Obtained
998 / EE / W 07 10 / 7020050 / 20100645		ADDITIONAL CREDIT		1800	69.33	1248	67
 ** 9 2010 SECRETARY MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION		PREVIOUS CREDIT		First Class		TOTAL CREDITS	200



क्रम संख्या/  
S. No. SSE/ 2007/ 403181

(07403)

# केन्द्रीय माध्यमिक शिक्षा बोर्ड Central Board of Secondary Education

M/07/07403/000371



सेकण्डरी स्कूल परीक्षा, 2007  
ALL INDIA SECONDARY SCHOOL EXAMINATION, 2007

यह प्रमाणित किया जाता है कि  
This is to certify that P S KARTHIKA

अनुक्रमांक Roll No. 4181643 आत्मज/आत्मजा श्रीमती P RAJLAKSHMI  
Son/Daughter of Smt.

एवं श्री R PALANISWAMY  
& Shri

जिनकी जन्म तिथि  
Born on TWENTY SECOND NOVEMBER NINETEEN HUNDRED NINETY ONE

है, ने बोर्ड द्वारा मार्च, 2007 में आयोजित सेकण्डरी स्कूल परीक्षा  
passed the Secondary School Examination of the Board held in March, 2007

विद्यालय से  
from GOVT MODEL SR SEC SCHOOL ABERDEEN PORT BLAIR A&N

निम्न विषयों में उत्तीर्ण की :-  
in the following subjects :-

ENGLISH COMM.

HINDI COURSE-A

MATHEMATICS

SCIENCE & TECH.

SOCIAL SCIENCE

दिल्ली  
Delhi  
दिनांक  
Dated

28-05-2007

*(M C Sharma)*  
(M C Sharma)

परीक्षा नियंत्रक  
Controller of Examinations





# ALAGAPPA UNIVERSITY

(Accredited with 'A' Grade by NAAC)

KARAIKUDI - 630 003.

REGISTER NO.

10C1120236

DATE

28/07/14

## PROVISIONAL CERTIFICATE

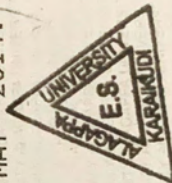
KARTHIKA P S

This is to certify that

has qualified for the Degree of B. A. (ENGLISH)

having passed the examination held in MAY 2014.

CLASS : THIRD.



Dy. Registrar

Dy. Controller

Dr. K. UTHAYASURIYAN

Controller of Examinations



CODE	SUBJECT	MARKS				RESULT	YEAR & MONTH
		I	E	T	In words		
11C	COMMUNICATION SKILLS	--	55	55	FIVE FIVE	Pass	MAY 2012
12	ENGLISH-PAPER - I	--	55	55	FIVE FIVE	Pass	MAY 2012
13	LITERARY FORMS	--	49	49	FOUR NINE	Pass	MAY 2012
14	ELIZABETHAN LITERATURE	--	40	40	FOUR ZERO	Pass	MAY 2012
15	RESTORATION LITERATURE	--	46	46	FOUR SIX	Pass	MAY 2012
21C	HUMAN SKILL DEVELOPMENT	--	48	48	FOUR EIGHT	Pass	DEC 2012
22	ENGLISH-PAPER - II	--	45	45	FOUR FIVE	Pass	DEC 2012
23	ROMANTIC LITERATURE	--	46	46	FOUR SIX	Pass	DEC 2012
24	VICTORIAN LITERATURE	--	42	42	FOUR TWO	Pass	DEC 2012
25	INDIAN ENGLISH LITERATURE	--	40	40	FOUR ZERO	Pass	DEC 2012
31	SHAKESPEARE	--	40	40	FOUR ZERO	Pass	DEC 2013
32	MODERN AND POST MODERN LITERATURE	--	44	44	FOUR FOUR	Pass	DEC 2013
33	AMERICAN AND COMMONWEALTH LITERATURES	--	45	45	FOUR FIVE	Pass	MAY 2014
34	ENGLISH FOR COMPETITIVE EXAMINATIONS	--	62	62	SIX TWO	Pass	DEC 2013
35	INTRODUCTION TO MASS COMMUNICATION	--	52	52	FIVE TWO	Pass	DEC 2013
TOTAL :	SEVEN ZERO NINE	709				CLASS	Third

ALAGAPPA NAGAR

KABAYIKINDI - 630 003

DATE: 28/07/14

## PASSING

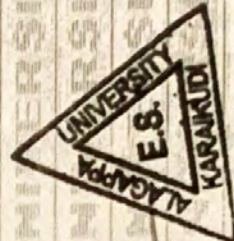
MARKS.

Written Paper

## Practical

Dissertation

((Abbreviation · I - Internal; E - Exam; T - Total))



Dv. Registrar

**Dy. Controller**

Dr. K. UTHAYASURTAN

## Controller of Examinations





अंडमान तथा निकोबार प्रशासन  
Andaman & Nicobar Administration  
डॉ भीमराव अंबेडकर प्रौद्योगिकी संस्थान  
DR.B.R.AMBEDKAR INSTITUTE OF TECHNOLOGY  
पहाडगाव, पोर्ट ब्लेयर - ७४४१०३ Pahargoon, Port Blair - 744103.  
अंडमान निकोबार दीप समूह Andaman & Nicobar Islands.



NO. 2-20/BRAIT/2015/ 1478

Dated 12 July, 2018.

**TO WHOM SO EVER IT MAY CONCERN**

This is to certify that **Ms. Karthika P.S** had been engaged as Guest Lecturer for B.Tech Programme in the Department of Electronics & Communication Engineering (ECE) of this Institute. She has rendered her services with effect from **12/01/2018 to 16/05/2018** during the Even Semester 2018-19.

I wish her all success for her future endeavors.



*[Handwritten Signature]*

**DEAN (ACADEMIC)**  
~~अ. त. न. प्रशासन~~  
DEAN (Academics)

डॉ. भीमराव अंबेडकर प्रौद्योगिकी संस्थान  
DR. B.R. AMBEDKAR INSTITUTE OF TECHNOLOGY  
पहाडगाव, पोर्ट ब्लेयर  
Pahargoon Port Blair - 744103